UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/816,255	04/01/2004	Andrew C. Davidson	5717-02000	9880
35690 7590 06/01/2007 MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C. P.O. BOX 398 PHAN, HANH			IINER	
			PHAN, HANH	
AUSTIN, TX 7	8767-0398		ART UNIT	PAPER NUMBER
			2613	
	•			
	•	·	MAIL DATE	DELIVERY MODE
			06/01/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

		·		1
		Application No.	Applicant(s)	-
		10/816,255	DAVIDSON, ANDREW C.	
	Office Action Summary	Examiner	Art Unit	
		Hanh Phan	2613	
5	The MAILING DATE of this communication	on appears on the cover sheet w	ith the correspondence address	
WHIC - Exte after - If NC - Failt Any	IORTENED STATUTORY PERIOD FOR R CHEVER IS LONGER, FROM THE MAILIN ansions of time may be available under the provisions of 37 Cr six (6) MONTHS from the mailing date of this communicati to period for reply is specified above, the maximum statutory ure to reply within the set or extended period for reply will, by reply received by the Office later than three months after the led patent term adjustment. See 37 CFR 1.704(b).	NG DATE OF THIS COMMUNI CFR 1.136(a). In no event, however, may a on. period will apply and will expire SIX (6) MOI statute, cause the application to become A	CATION. reply be timely filed  ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).	
Status				
1)	Responsive to communication(s) filed on	01 April 2004.		
2a)□		This action is non-final.		
• —	Since this application is in condition for all closed in accordance with the practice un	•		
Disposit	ion of Claims		•	
5)	Claim(s) 1-26 is/are pending in the application 4a) Of the above claim(s) is/are with Claim(s) is/are allowed.  Claim(s) 1-26 is/are rejected.  Claim(s) is/are objected to.  Claim(s) are subject to restriction and claim(s) are subject to restriction are subject to restriction and claim(s) are subject to restriction and claim(s) are subject to restriction and claim(s) are subject to restriction are subject to restriction and claim(s) are subject to restriction are subject to restriction are subject to	thdrawn from consideration.		
Applicat	ion Papers		·	
10)⊠	The specification is objected to by the Example The drawing(s) filed on <u>01 April 2004</u> is/ar Applicant may not request that any objection to Replacement drawing sheet(s) including the of the oath or declaration is objected to by the	re: a) $\square$ accepted or b) $\square$ objeto the drawing(s) be held in abeya correction is required if the drawing	nce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1.121(d).	
Priority (	under 35 U.S.C. § 119			
12)□ a)	Acknowledgment is made of a claim for form All b) Some * c) None of:  1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International B	ments have been received. ments have been received in A e priority documents have beer sureau (PCT Rule 17.2(a)).	opplication No  received in this National Stage	
2) Notice	nt(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-94 mation Disclosure Statement(s) (PTO/SB/08)	18) Paper No	Summary (PTO-413) s)/Mail Date nformal Patent Application	
	er No(s)/Mail Date	6)  Other:	• •	

S. Patent and Trademark Office OL-326 (Rev. 08-06)

Application/Control Number: 10/816,255

Art Unit: 2613

## **DETAILED ACTION**

1. Applicant's election without traverse of Group I directed to claims 1-26 in the reply filed on 04/24/2007 is acknowledged. And, Claims 27-34 are cancelled.

## Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Watanabe (US Patent No. 5,896,211).

Regarding claims 1 and 15, referring to Figure 4, Watanabe teaches a system for use in optical measurement and/or inspection of sub-surface features in layered media, the system comprising:

an optical-to-electrical (OE) circuit (i.e., optical detector 35, Fig. 4) configured to convert an optical signal into a first electrical signal, wherein the optical signal includes a plurality of wavelengths (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50);

a demodulating circuit (i.e., demodulators 37-1 to 37-m, Fig. 4), wherein the demodulating circuit is coupled to receive the first electrical signal from the OE circuit and a demodulating signal, and wherein the demodulating circuit is further configured to provide as an output a second electrical signal, wherein the demodulating signal and

Application/Control Number: 10/816,255

Art Unit: 2613

the second electrical signal each correspond to one of the plurality of wavelengths (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claims 2 and 16, Watanabe further teaches the system further includes an output optics unit coupled to provide the optical signal to the OE circuit, wherein the output optics unit is coupled to receive a beam of light (i.e., Fig. 1, col.1, lines 65-67 and col. 2, lines 1-14).

Regarding claims 3, 4 and 17, Watanabe further teaches the beam of light is a reflected beam of light or is a diffracted beam of light (i.e., Figs. 1 and 4).

Regarding claims 5 and 18, Watanabe further teaches the output optics unit is coupled to provide the optical signal to a plurality of OE circuits, wherein each of the OE circuits is coupled to one of a plurality of demodulating circuits, and wherein the plurality of OE circuits and the plurality of demodulating circuits form a demultiplexer (i.e., Figs. 1, 4, 5, 16 and 17, col.1, lines 65-67, col. 2, lines 1-14, col. 4, lines 52-67, col. 5, lines 1-67 and col. 6, lines 1-32).

Regarding claims 6 and 19, Watanabe further teaches the system further includes an optical multiplexer (i.e., optical mixer 38, Fig. 4), wherein the optical multiplexer is coupled to receive a plurality of light beams, wherein each of the plurality of light beams has a different wavelength with respect to other ones of the plurality of light beams (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claims 7 and 20, Watanabe further teaches the optical multiplexer (i.e., optical mixer 38, Fig. 4) is coupled to a plurality of light sources (i.e., light sources

Application/Control Number: 10/816,255

Art Unit: 2613

33, Fig. 4), wherein each of the plurality of light sources provides one of the plurality of light beams (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claims 8 and 21, Watanabe further teaches each of the plurality of light sources is coupled to a modulator, wherein the modulator is configured to provide a modulating signal (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claims 9 and 22, Watanabe further teaches each of the plurality of light sources is modulated by a directly modulated diode (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claims 10 and 23, Watanabe further teaches the optical multiplexer (i.e., optical mixer 38, Fig. 4) is positioned to project an incident light beam onto a surface, wherein the incident light beam includes wavelengths corresponding to each of the plurality of light beams (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claims 11-13 and 24-26, Watanabe further teaches the optical multiplexer (i.e., optical mixer 38, Fig. 4) performs frequency division multiplexing and the demultiplexer performs frequency division demultiplexing or the optical multiplexer performs time division multiplexing and the demultiplexer performs time division demultiplexing or the optical multiplexer performs code division multiplexing and the demultiplexer performs code division multiplexing and the demultiplexer performs code division demultiplexing (i.e., Fig. 4, col. 4, lines 52-67 and col. 5, lines 1-50).

Regarding claim 14, Watanabe further teaches the system is implemented in a lithography system (i.e., Figs. 1-24).

Application/Control Number: 10/816,255 Page 5

Art Unit: 2613

## Conclusion -

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Puc et al (US Patent No. 6,604,872) discloses optical communication systems.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hanh Phan whose telephone number is (571)272-3035.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jason Chan, can be reached on (571)272-3022. The fax phone number for the organization where this application or proceeding is assigned is (571)273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)305-4700.

HANH **PHAN** PRIMARY **EXAMINER**